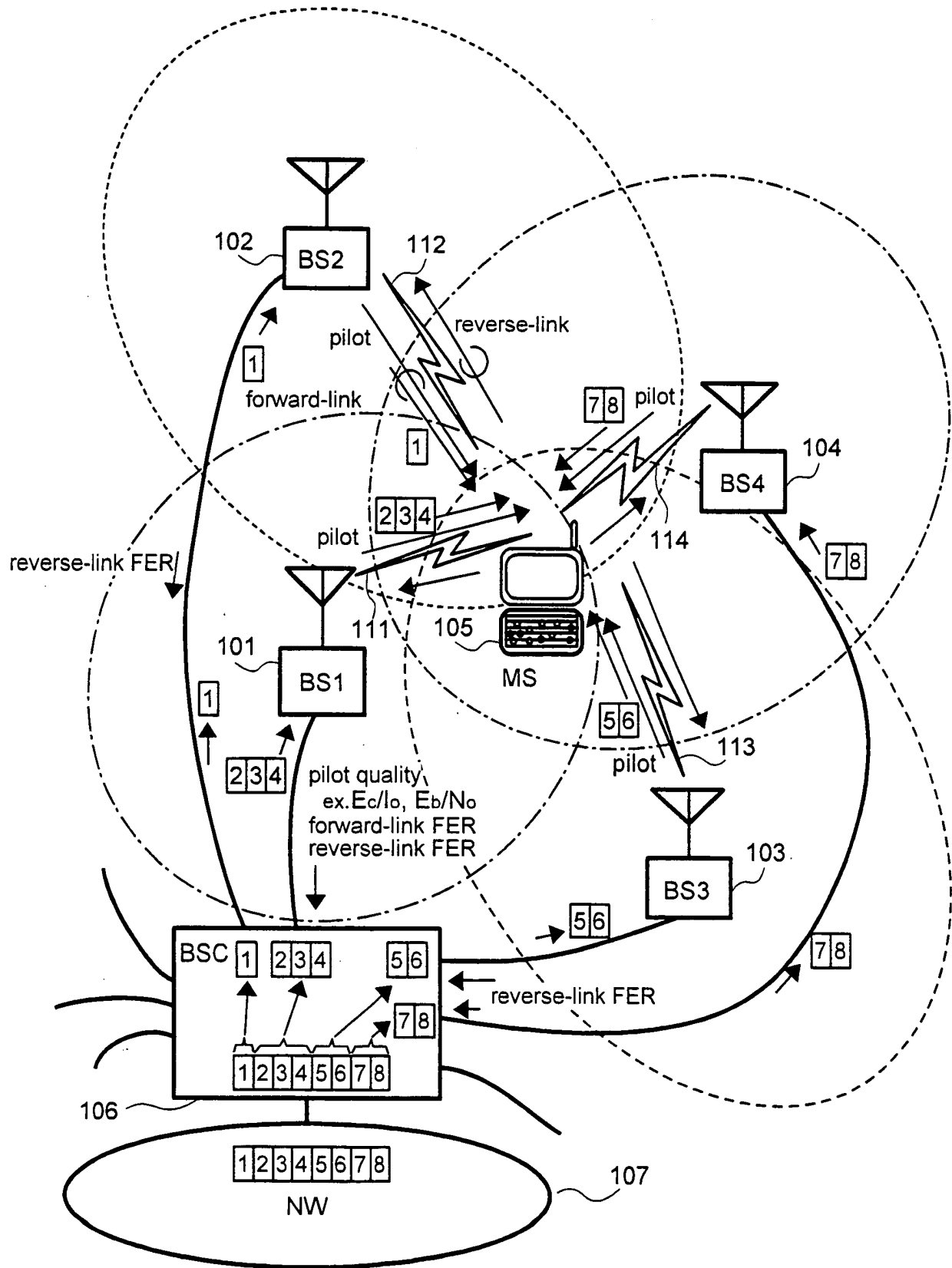


FIG.1



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FIG.2

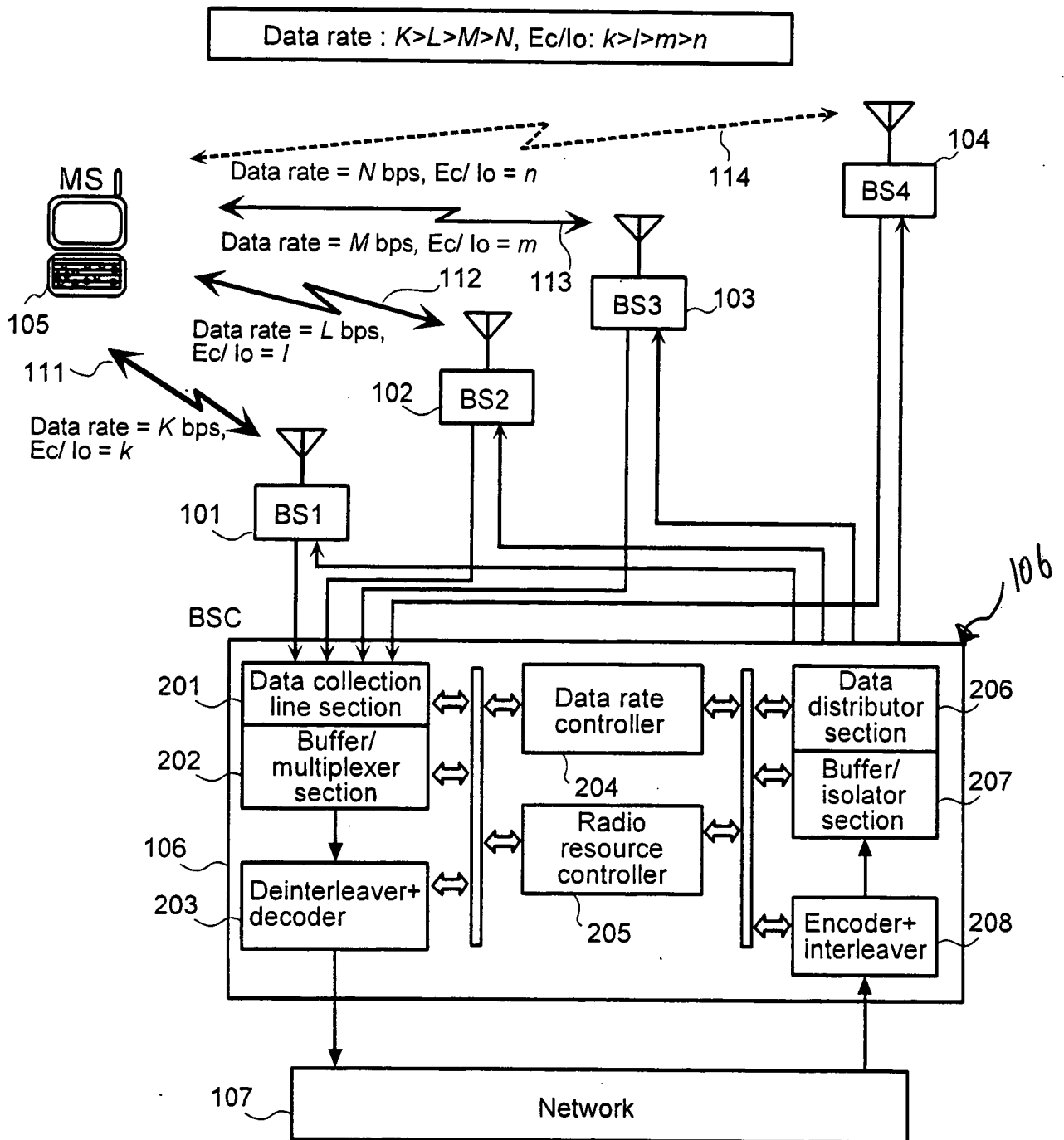
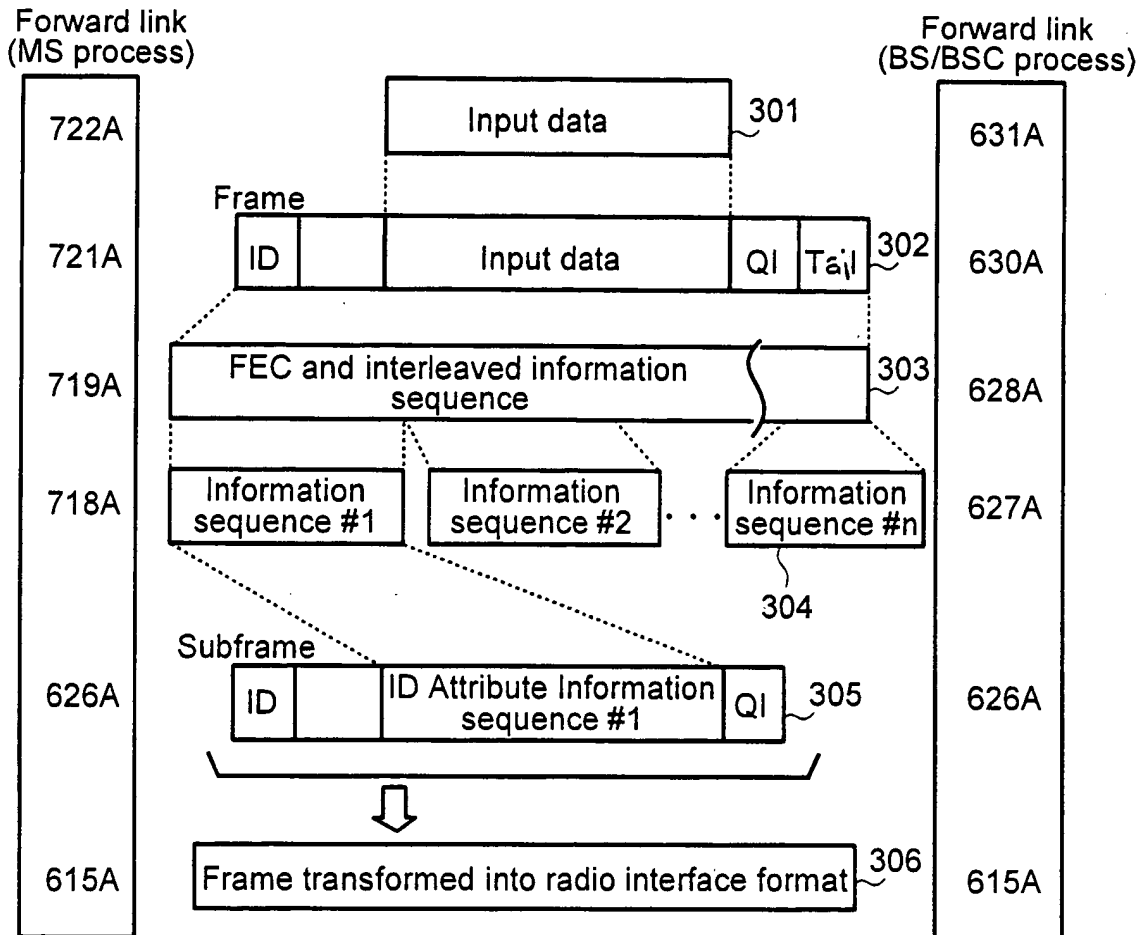


FIG.3



Note :

- QI=quality information
- FEC=Forward error correction
- Tail= Appened bit for FEC
- ID=Identifier

FIG.4

106

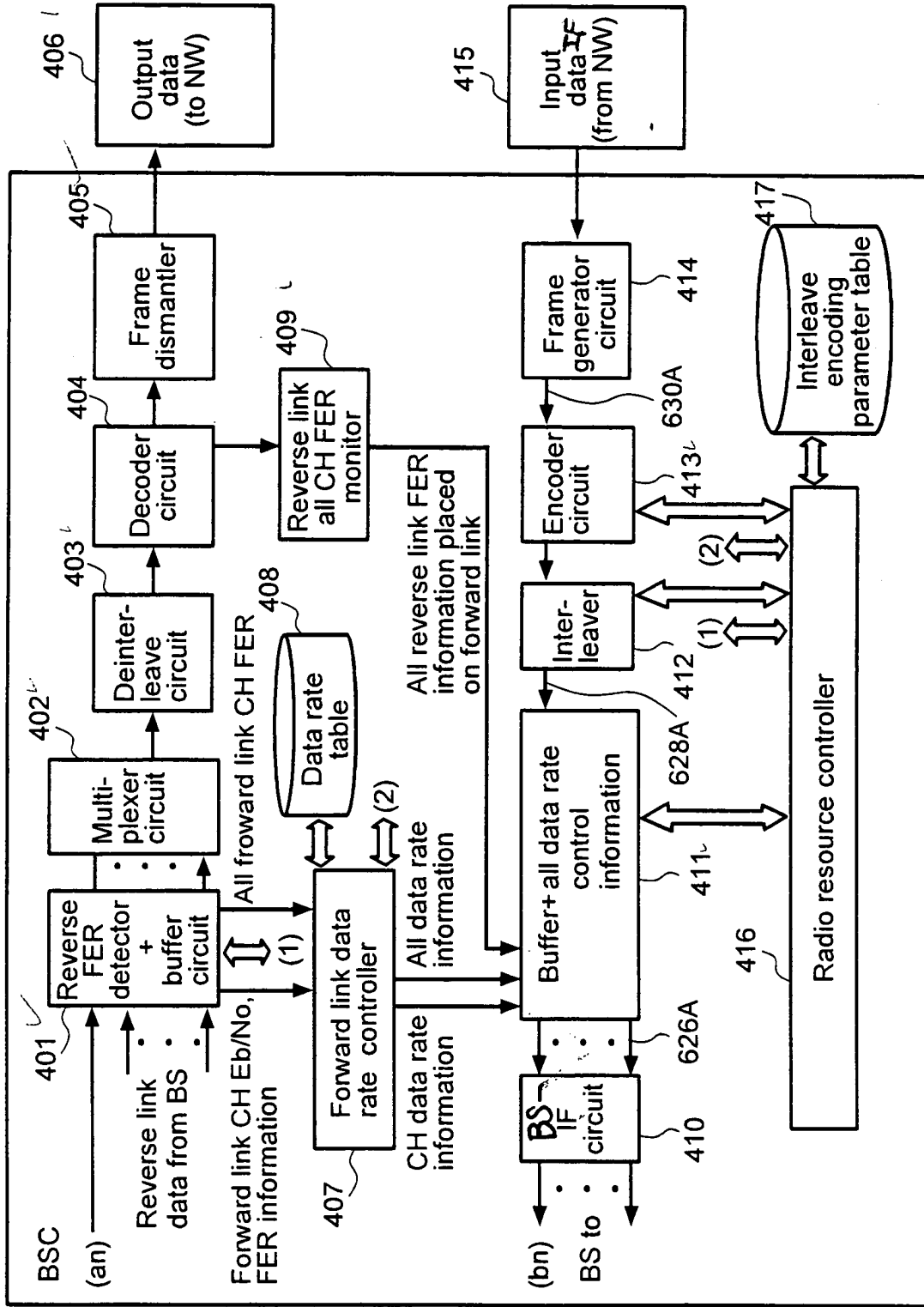


FIG.5

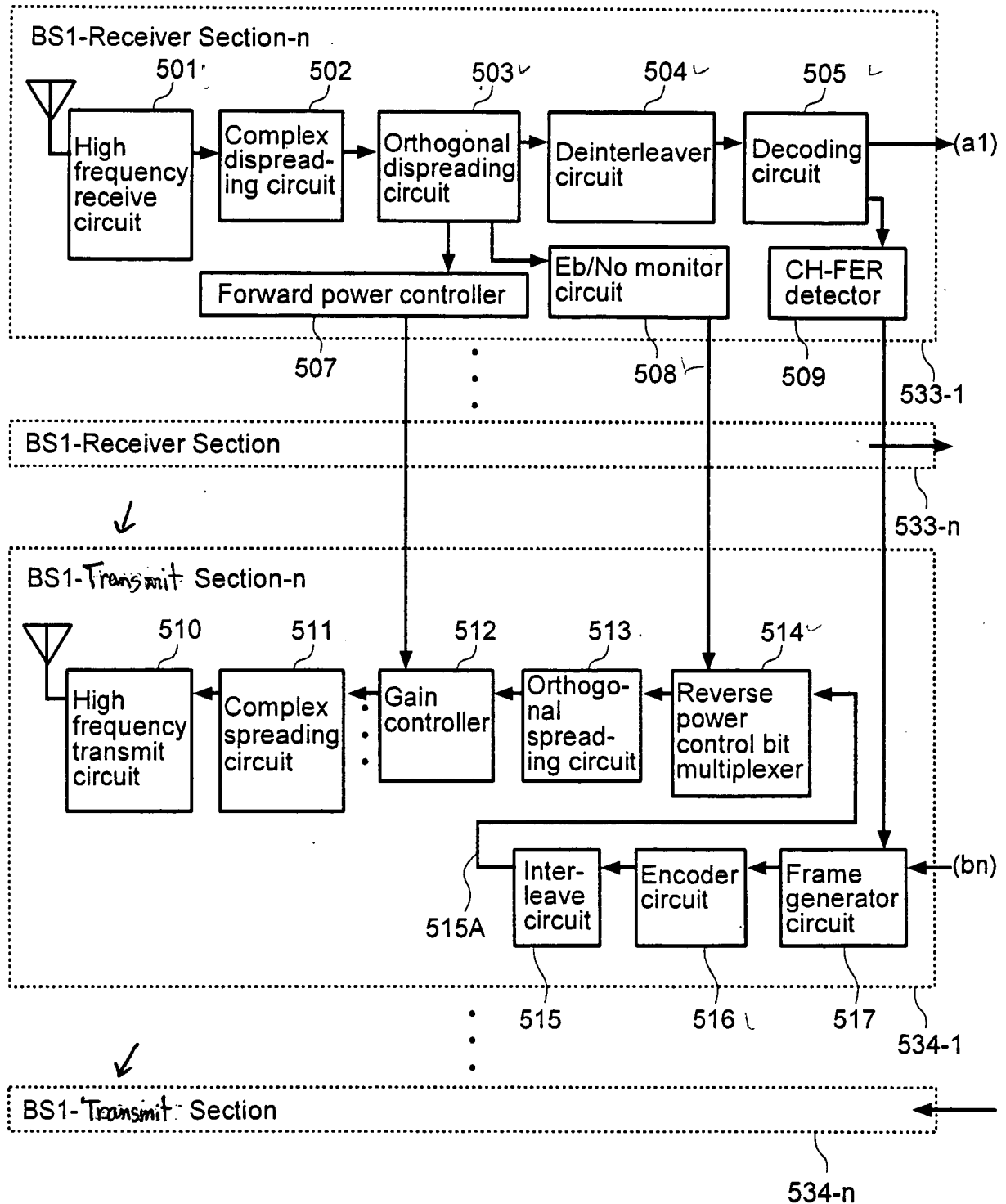
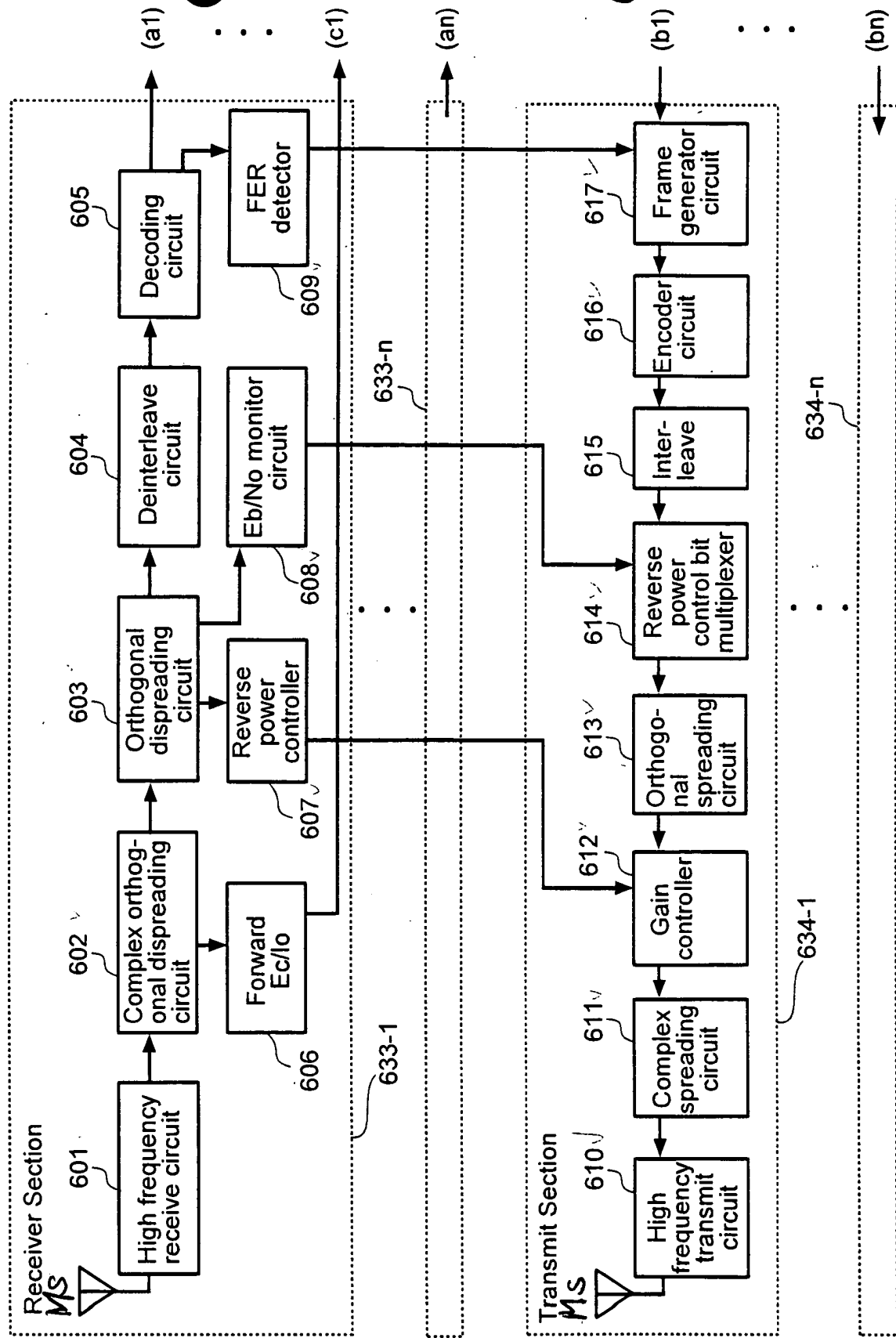


FIG. 6



735

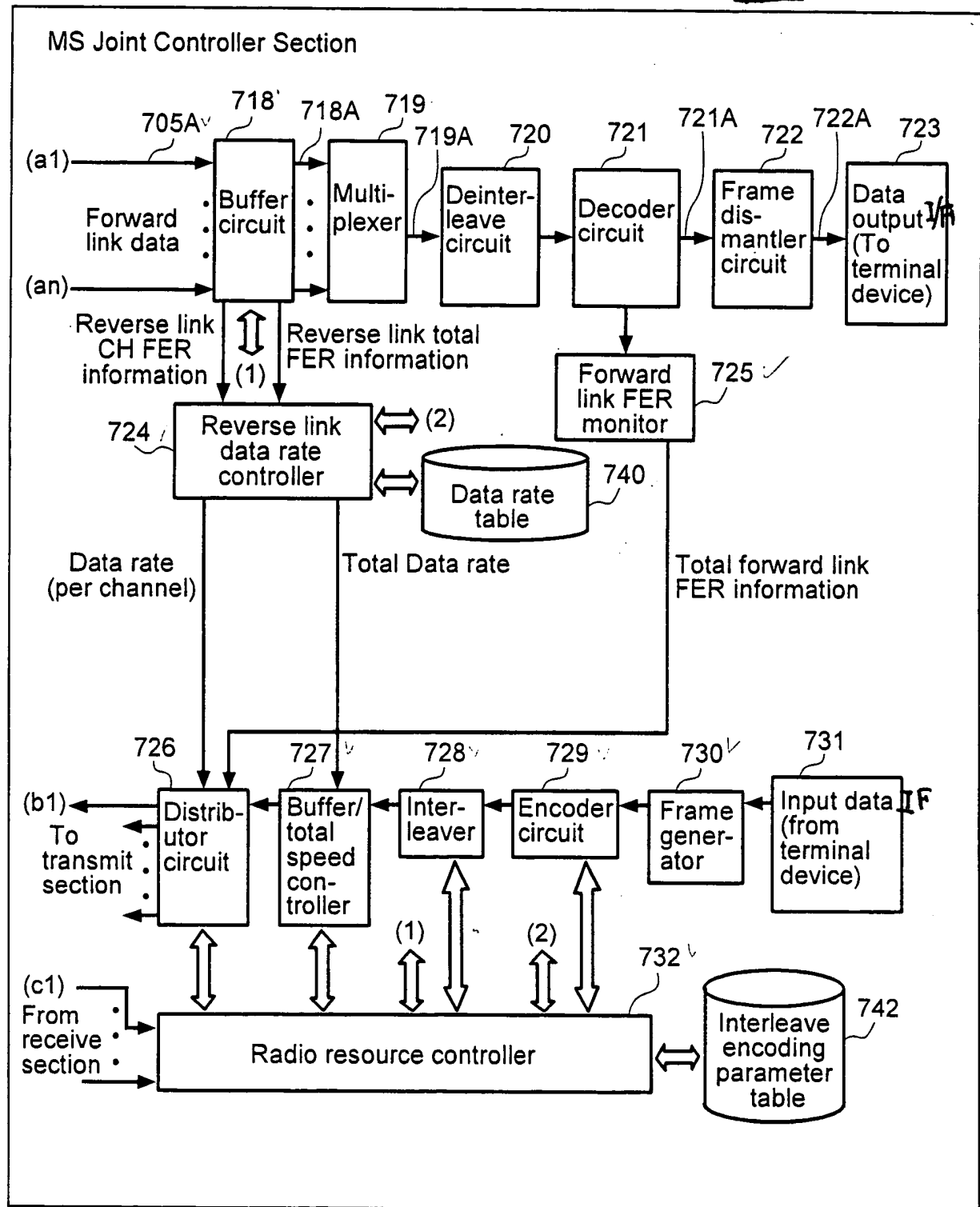


FIG.8

(a) Data rate parameters per line

| | | | | | |
|------------------------------------|--|-----|------|-----|-----|
| Lowest E_c/I_0 | s | i | | v | w |
| Lowest E_b/N_0 | k | l | | m | n |
| Maximum allowable data rate per CH | K | L | | M | N |
| FER per line | Use for rating the quality of each line For instance, lower the data rate if the FER drops below the standard value. | | | | |

(b) Total data rate parameters after multiplexing

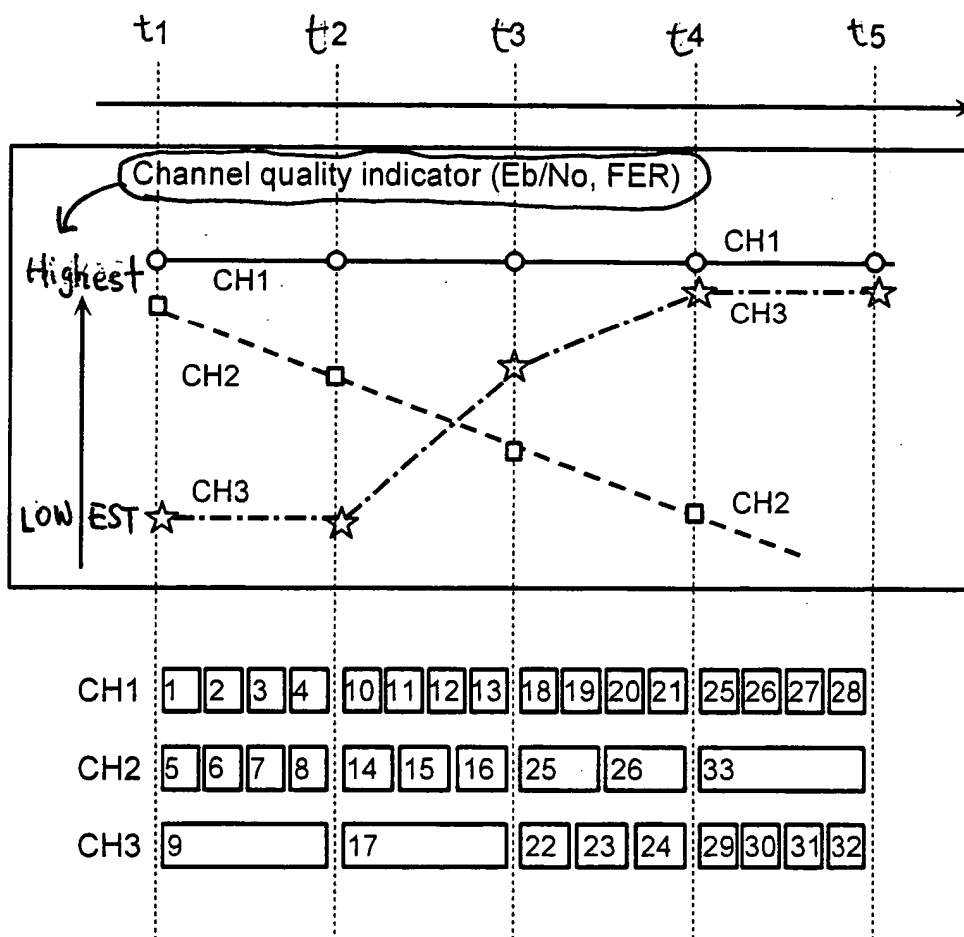
| | | | | | |
|--|---|-----|------|-----|-----|
| Estimated data rate total sum per line | a | b | | c | d |
| Interference margin coefficient | A specified line shall be used, when the use of the same frequency or time slot is allowed between circuits. This depends on the line reserve method and the extent of the interference margin of the system. | | | | |
| Maximum allowable data rate (after multiplexing) | W | X | | Y | Z |
| FER after multiplexing | Use for rating the quality of each line after multiplexing. For instance, lower the data rate if the FER drops below the standard value. | | | | |

(c) Interleave size parameters of multiplexed signal

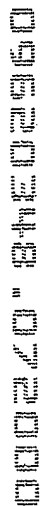
| | | | | | |
|---|-----|-----|------|-----|-----|
| Number of connected lines | a | b | | c | d |
| Data rate ratio between circuits | p | q | | r | s |
| Interleave size (number of frames for interleave) | s | t | | v | w |

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FIG.9



000220" 84E02960



102 106

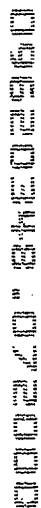
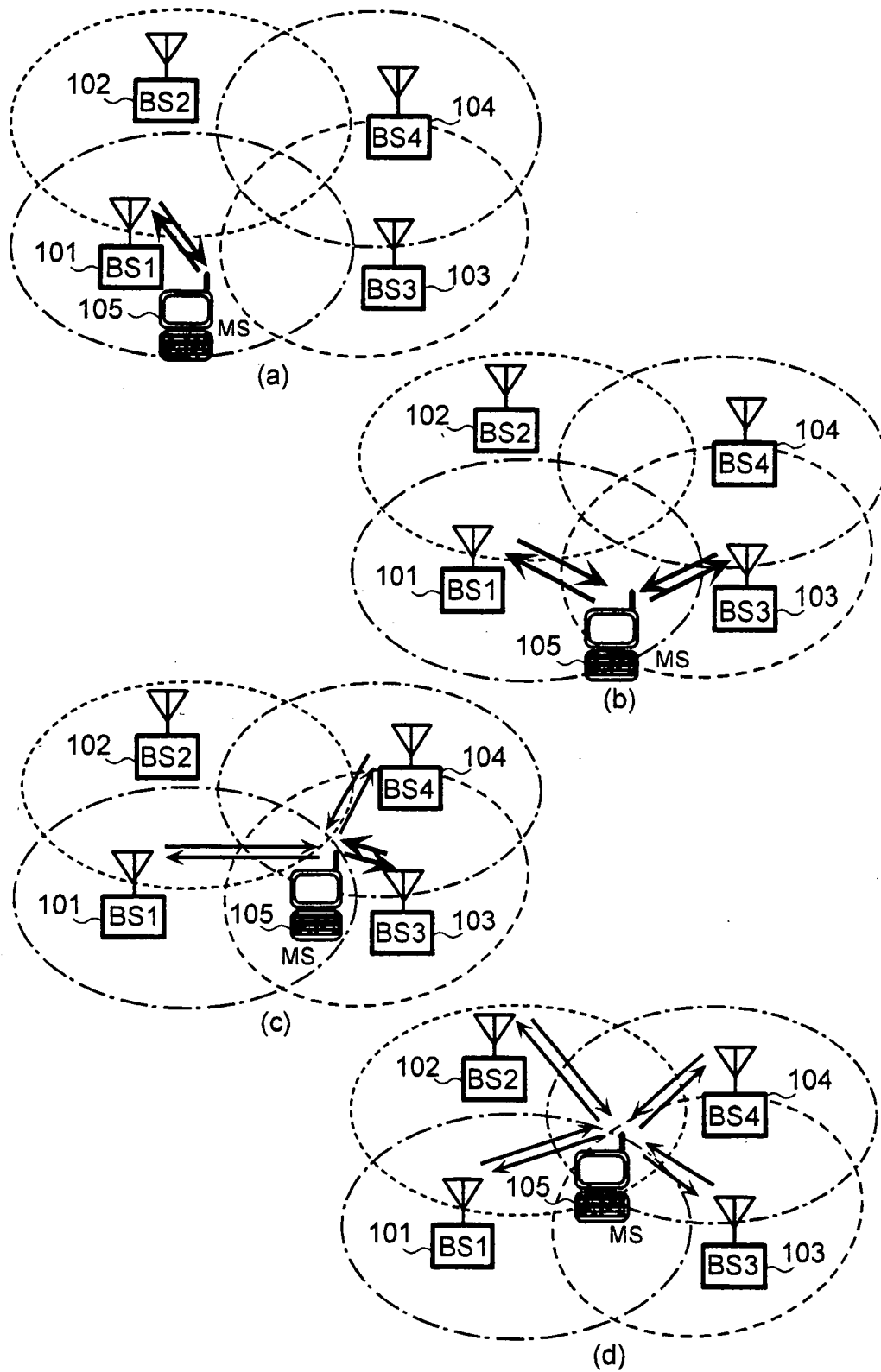
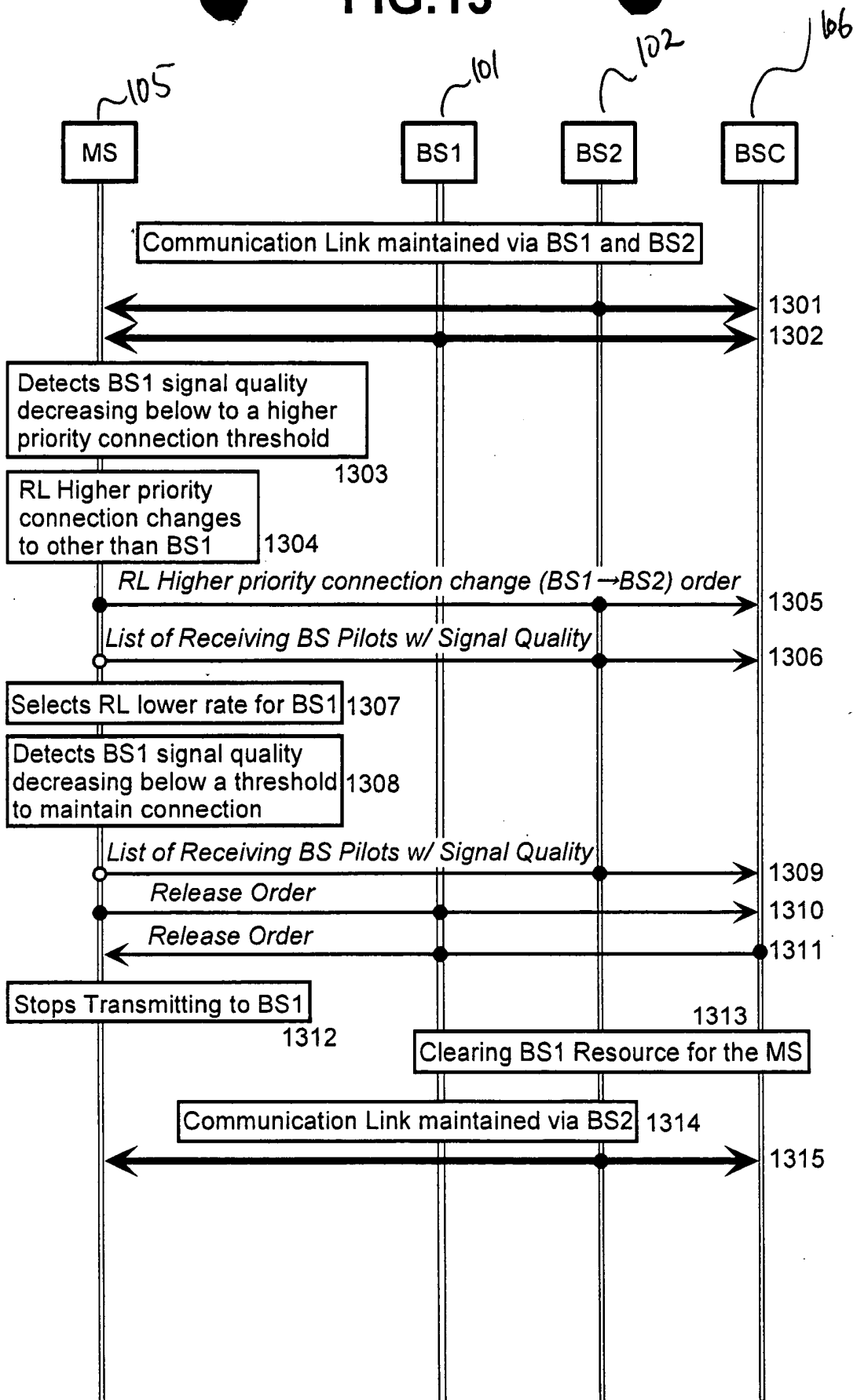


FIG.12



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FIG.13



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